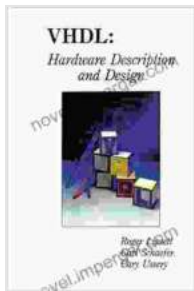


# Hardware Description and Design: A Pragmatic Approach

This book provides a pragmatic approach to hardware description and design. It starts with the basics and gradually builds up to complex concepts, with a focus on real-world applications.



## VHDL: Hardware Description and Design (NATO Asi Ser C: Mathematical and) by Roger Lipsett

★★★★★ 5 out of 5

Language : English

File size : 2997 KB

Text-to-Speech : Enabled

Screen Reader : Supported

Print length : 319 pages



The book is divided into three parts:

1. **Part 1: Foundations**
2. **Part 2: Design**
3. **Part 3: Verification**

Part 1 covers the basics of hardware description and design, including:

- Digital logic
- Data representation

- Combinational circuits
- Sequential circuits

Part 2 covers the design of hardware systems, including:

- System architecture
- State machines
- Memory
- Input/output

Part 3 covers the verification of hardware systems, including:

- Simulation
- Testing
- Formal verification

## **Benefits of Using This Book**

This book provides a number of benefits for readers, including:

- **A comprehensive overview of hardware description and design**
- **A focus on real-world applications**
- **Clear and concise explanations**
- **Numerous examples and exercises**

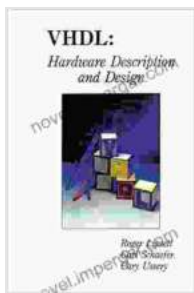
## **Who Should Read This Book?**

This book is ideal for:

- **Students**
- **Engineers**
- **Anyone interested in learning about hardware description and design**

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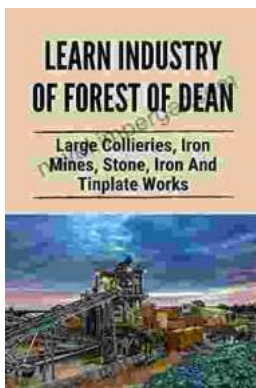
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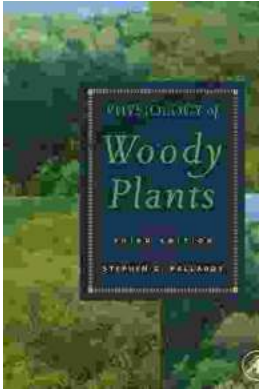
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